Rev. 03/05/04

## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

ipplicants: Fabrizio Petrini

Docket No.: S-94,651

Serial No.:

09/895,570

Examiner:

Wu, Qing Yuan

Filed

6/28/2001

Art Unit:

2194

For

BUFFERED COSCHEDULING FOR PARALLEL PROGRAMMING

AND ENHANCED FAULT TOLERANCE

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

## <u>INFORMATION DISCLOSURE STATEMENT</u> <u>UNDER 37 CFR 1.56, 1.97, AND 1.98</u>

Sir:

The documents listed below are identified in compliance with the duty of disclosure defined in 37 CFR 1.56. Copies are attached as appropriate.

- Diefendorff, "Compaq Chooses SMT for Alpha: Simultaneous Multithreading Exploits Instruction – and Thread-Level Parallelism," Microprocessor Report, Vol. 13, No. 16, December 1999, pp. 1-7.
- Keckler et al., "Concurrent Event Handling through Multithreading,"
   IEEECS, Vol. 48, No. 9, September 1999, pp. 903-916.

CERTIFICATE OF MAILING/TRAN I hereby certify that this correspondence is, on the date shown be	
MAILING  ☑ deposited with the United States Postal Service with sufficient postage as first class mailin an envelope addressed to the:  Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.	FACSIMILE  trensmitted by facsimile to the United States Patent and Trademark Office.
DateAugust 2, 2005	Signature  Mark N. Fitzgerald  (type or print name of person certifying)

- Tseng et al., "All-to-All Personalized Communication in a Wormhole-Routed Torus," IEEECS, Vol. 7, No. 5, May 1996, pp. 490-505.
- 4. Arpaci-Dusseau et al., "Scheduling with Implicit Information in Distributed Systems," June 1998, pp. 1-11.
- 5. Eggers et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors, "IEEE Micro, September/October 1997, pp. 12-19.
- 6. Raz et al., "Dynamic Load Balancing," US Patent 6,173,306, Issued January 9, 2001.
- 7. Kato et al., "Ring Systolic Array System for Synchronously Performing Matrix/Neuron Computation Using Data Transferred Through Cyclic Shift Register Connected in Cascade of Trays," US Patent 5,600,843, Issued February 4, 1997.

This Information Disclosure Statement is not to be construed as a representation that a search has been made or that additional matter material to the examination of this application does not exist. Applicant does not believe that any of these citations constitutes prior art under 35 U.S.C. 102.

It is requested that the above citations be made of record in the prosecution of this application.

Date: August 2, 2005

Reg. No. 48,300 Phone (505) 665-5187 Mark N. Fitzgerald Los Alamos National Laboratory LC/IP, MS A187

Respectfully submitted,

Los Alamos, New Mexico 87545



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Application Number		6/28/2001	
					CANT	Filing Date First Named Inventor Art Unit		Fabrizio Petrini 2194	
(use as m	nany she	ets as necessa	ary)				ner Name	Qing Yuan Wu	
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Examiner	Cite	Dogument Numb	•	2	Publication D		Name of Patentee or	Pages, Columns, Lines, Where	
Initials	No. 1	Document Numb Number - Kind C known)	Code <sup>2</sup> (if		MM-DD-YY		Applicant of Cited  Document	Relevant Passages or Relevant Figures Appear	
		US-6,173,3	306		01/09/2001		RAZ et al.		
		US-5,600,8	343		02/04/1997		KATO et al.		
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Examiner Initials	Cite No.	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> ( <i>if known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T€
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Examiner Signature			Date Consider	ed	<u>                                     </u>	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant..

Applicant's unique citation designation number (optional).

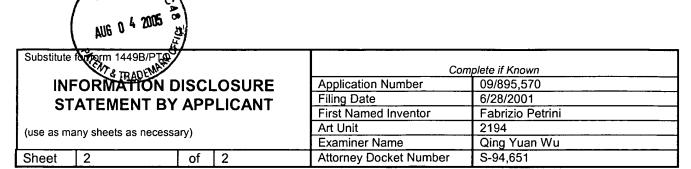
See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

Senter Office that issued the document, by the two-letter code (WIPO Standard ST.3).

For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

Applicant is to place a check mark here if English language Translation is attached.



		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	T²					
Examiner Initials	Cite No. <sup>1</sup>							
		Diefendorff, "Compaq Chooses SMT for Alpha: Simultaneous						
		Multithreading Exploits Instruction – and Thread-Level Parallelism,"						
		Microprocessor Report, Vol. 13, No. 16, December 1999, pp. 1-7.						
		Keckler, et al., "Concurrent Event Handling through Multithreading,"						
		IEEECS, Vol. 48, No. 9, September 1999, pp. 903-916.						
		Tseng et al., " All-to-All Personalized Communication in a						
		Wormhole-Routed Torus," IEEECS, Vol. 7, No. 5, May 1996,pp. 490-505.						
		Arpaci-Dusseau, et al., "Scheduling with Implicit Information in Distributed Systems," June 1998, pp. 1-11.						
		Eggers, et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors, " IEEE Micro, September/October 1997, pp. 12-19.						
Examiner Signature		Date Considered						

<sup>&</sup>lt;sup>1</sup> Applicant's unique citation designation number (optional). T<sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.